Ethan Chung

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EDUCATION

University of Minnesota, Twin Cities — **Minneapolis, MN**
College of Science and Engineering | Honors Program — *June 2028*
Bachelor of Electrical Engineering — GPA: 3.92
3x Dean's List Recipient
Woodbury High School: Graduated with Summa Cum Laude Honors — GPA: 3.99

PROFESSIONAL SUMMARY

Electrical Engineering student (GPA 3.92) with hands-on experience spanning nanofabrication, embedded systems, and startup engineering leadership. Co-Founder and CTO of Anoura, a B2B analytics platform for multi-location dental service organizations, leading technical strategy and product development in partnership with a 13-location dental group. Board member and a founder of UMN's ASIC Club, doing RTL design and verification for a custom systolic array fabbed through Tiny Tapeout, and previously as Nanofabrication Lead on the UMN Neuron Interface Project, with expertise in PCB design (KiCAD, Altium, LTSpice), microelectrode array (MEA) fabrication in cleanroom environments, and neural stimulation circuitry using DACs. Brings process engineering precision from Maxwell Labs and the Minnesota Nanotechnology Center to a track record of translating complex technical problems into shipped systems, with strong communication and leadership skills that drive progress across research, engineering, and founding teams.

LEADERSHIP EXPERIENCE

UMN ASIC Club (Sone Arch Silicon): Technical Lead , Minneapolis, Minnesota — *March 2026 – Present*
- Developing a custom 2x2 systolic array to be fabbed in the Tiny Tapeout program
- Plan and coordinate events with other UMN bodies such as the Minnesota Nano Center
- RTL design and test bench creation verification in Verilog

PROFESSIONAL EXPERIENCE

Maxwell Labs: Electrical Engineering and Process Engineer Intern / Part Time, Minneapolis, Minnesota — *May 2026 – Present*
- Developed a process to create quartz thin film (sub 5um thickness) microfluidic wells to hermetically contain dye for ASPL experiments
- Using the following techniques: Sputtering, photolithography, RIE, wet etching, fusion bonding, characterization tools

Anoura: CTO and President, Minneapolis, Minnesota — *September 2026 – Present*
- Co-founded a B2B analytics platform serving multi-location dental service organizations (DSOs), building a unified operational data layer across practice management and payroll systems (Greyfinch, ADP, custom software)
- Established a design partnership with a 13-location Twin Cities orthodontic group, driving product requirements directly from a live multi-location practice (Minnesota Orthodontics)

Minnesota Nanotechnology Center Undergraduate Laboratory Assistant, Minneapolis, Minnesota — *August 2025 – September 2026*
- Run deposition and etching tests on equipment in the clean rooms
- Ensure safe handling and disposal of hazardous chemicals and waste

Nanofabrication Lead, UMN Neuron Interface Project, Minneapolis, Minnesota — *September 2024 – February 2026*
- Led fabrications of Micro electrode Arrays (MEAs) in clean room using complex machinery and procedures
- Experience with developing circuitry using DACs to stimulate cortical neurons

PROJECT EXPERIENCE

NeuSleep: EEG sensing headband with Bluetooth capabilities — *February 2026 – Present*
- Designed a 6 layer PCB with the purpose of collecting EEG data and sending it via bluetooth (ADS1299 and NRF52840)
- Implemented firmware in C via VSCode (nrf connect)

Bright Manufacturing Challenge: Line Following Robot — *July 2025 – October 2025*
- Designed and assembled a custom PCB integrating motor drivers, IR sensors, and an STM32 microcontroller for autonomous line detection and control
- Developed embedded firmware in C using STM32CubeIDE, implementing real-time PID control, sensor processing, and motor PWM modulation

AWARDS

1st Place (PCB Design) and 4th Overall SMTA International Bright Manufacturing Challenge: Line Following Robot — *October 2025*
2x $3,000 UMN MVP Grant (MIN-CORP) — *October 2024 & 2025*

SKILLS

Technical Skills: Verilog, Nanofabrication procedures (Photolithography, Electron Beam Evaporation, RIE, plasma enhanced chemical vapor deposition, reactive ion etching), Claude Code, Verilog, LTSpice, Altium, KiCAD, MEAs, STM32 and NRF Embedded, Onshape, C++, Python, Javascript, Java, BLE (Bluetooth Low Energy)